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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.:

10/066,188

Filing Date:

1/31/02

For:

METHOD AND APPARATUS FOR

SPECIFYING ENCODED SUB-

NETWORKS

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This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a

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Dated: December 1, 2003

Respectfully ubmitted.

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Serial No. 10/066,188 Docket No. SPLX.P0095 Approved for use through 05/31/2003. OMB 0651-0031

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	(us	se as many sheets a	s nece	ssary)	Examiner Name				
	Sheet	1	of	1	Attorney Docket Number	SPLX.P0095			

				U.S.	PATENT DOCUMENTS	
Examiner* Initials	Cite No.1	Number Kind	ument d Code ² known)	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	5,519,630		05-21-1996	Nishiyama et al.	
	2.	5,537,341		07-16-1996	Rose et al.	DECEMED
	3.	5,696,974		12-09-1997	Agrawal et al.	RECEIVED
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NON PATENT LITERATURE DOCUMENTS						
Examiner* Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶			
	8.	Crastes, M. et al., A Technology Mapping Method Based on Perfect and Semi-Perfect Matchings, Design Automation Conference, 1991, 28 th ACM/IEEE, June 17-21, 1991, pp. 93-98.				
	9.	Devadas, S. et al., Synthesis and Optimization for robustly delay-fault testable combinational logic circuits, Design Automation Conference, 1990, 27 th ACM/IEEE, June 24-28, 1990, pp. 221-227.				

Examiner	Date	-
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